

[PHNL020054]

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Amendment to the Claims:

Please amend the claims as follows:

1. (Currently Amended) A method of extracting data from a received analogue signal, the received analogue signal having a preamble of a predetermined preamble frequency and a predetermined preamble duration, and a data portion with the data, the data portion having a predetermined data rate, the method comprising
 - obtaining a signal representing a DC value (V_{dc}) of the received signal,
 - comparing the received analogue signal to the signal representing a DC value (V_{dc}) of the received signal, and
 - generating, in dependence on the comparison of the received analogue signal to the DC value (V_{dc}) of the received signal, a digital bit stream,characterized in that, prior to obtaining the signal representing a DC value (V_{dc}) of the received signal, the received signal is filtered so as to reject the predetermined preamble frequency.
2. (Previously presented) A method according to claim 1, characterized in that the signal representing a DC value (V_{dc}) of the received signal is obtained using a low pass filter.
3. (Previously presented) A method according to claim 2, characterized in that the low pass filter is switchable between a first cut-off frequency and a second cut-off frequency lower than the first cut-off frequency, and that during reception of the preamble the low pass filter is switched to the first cut-off frequency, and that during reception of data the low pass filter is switched to the second cut-off frequency.
4. (Previously presented) A method according to any one of claims 1-3, characterized in that the received analogue signal is a demodulated signal.
5. (Previously presented) A data slicer circuit for extracting data from a received analogue signal, the received analogue signal having a preamble of a predetermined preamble frequency

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and a data portion with the data, the data portion having a predetermined data frequency, the circuit comprising

- a low pass filter for obtaining a signal representing a DC value (Vdc) of the received signal,
- a comparator for comparing the received analogue signal to the signal representing a DC value (Vdc) of the received signal, and for generating, in dependence on the comparison of the received analogue signal to the DC value (Vdc) of the received signal, a digital bit stream, characterized in that, a filter for rejecting the predetermined preamble frequency is coupled to receive the received analogue signal and to feed a rejection filtered signal to the low pass filter.

6. (Previously presented) A data slicer circuit according to claim 5, characterized in that the low pass filter is switchable between a first cut-off frequency and a second cut-off frequency lower than the first cut-off frequency, and that during reception of the preamble the low pass filter is switchable to the first cut-off frequency, and that during reception of data the low pass filter is switchable to the second cut-off frequency.

7. (Previously presented) A data slicer circuit according to any one of claims 5-6, characterized in that the filter for rejecting the predetermined preamble frequency is a notch filter.

8. (Previously presented) A data slicer circuit according to claim 7, characterized in that the notch filter is a first order notch filter with a 3 dB bandwidth equal to its frequency of maximum rejection.